

HiDFT®-STAR

Generic RTL to RTL Editing of Complex Designs



DATASHEET

Overview

HiDFT-STAR (High level Design For Test – Structured Test ARchitectures) is a fully customizable RTL-to-RTL editing tool for IP cores and SOCs. HiDFT-STAR works seamlessly with HiDFT-SIGNOFF and helps automating the integration process at RTL. This silicon proven product has successfully helped moving the integration of memory BIST test logic from gate to RTL.

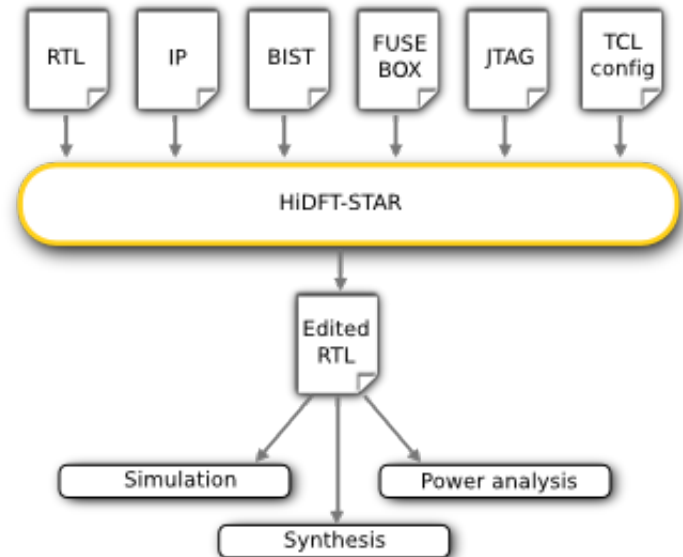
Key Features

- Design Rule Checks (DRC) on RTL input
- Flexible set of commands for design querying
- Comprehensive set of commands for incremental editing
- Multiple connection techniques
 - Change driver / load of arbitrary node in the design hierarchy
 - Create connectivity between arbitrary nodes in the design hierarchy
- Powerful tracing signal mechanism across the design hierarchy
- Effective mechanism for connectivity checking
- Easy development of custom Tcl applications
- Silicon proven on multiple designs

Key Benefits

- Fast and error-free insertion of RTL structures
- One software platform for several DFT applications
- Significant reduction of TAT (Turn Around Time)

Methodology



Headquartered in Moirans (Grenoble area, France), DeFacto Technologies is an innovative chip design software company developing breakthrough technology to dramatically enhance the DFT process and increase the testability of integrated circuits (ICs) and systems on a chip (SoCs).

Our mission is to enable designers to complete planning, analysis and insertion of integrated circuit test logic before synthesis by delivering a high quality suite of tools working at the register transfer level (RTL).

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